



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 5327

SERIAL NUMBER 10/648,401	FILING DATE 08/27/2003 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 053785-5148
-----------------------------	---------------------------------------	--------------	------------------------	---------------------------------------

APPLICANTS

Seok-Woo Lee, Gyeonggi-do, KOREA, REPUBLIC OF;

~~SIX~~ ** CONTINUING DATA ***** *none*

** FOREIGN APPLICATIONS ***** *SIX*
 REPUBLIC OF KOREA 2002-53086 09/04/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/19/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 9	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Harold Shaw</i> Examiner's Signature Initials				

ADDRESS

09629
 MORGAN LEWIS & BOCKIUS LLP
 1111 PENNSYLVANIA AVENUE NW
 WASHINGTON, DC
 20004

TITLE

Method of fabricating top gate type thin film transistor having low temperature polysilicon

FILING FEE RECEIVED 858	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
-----------------------------------	---	---